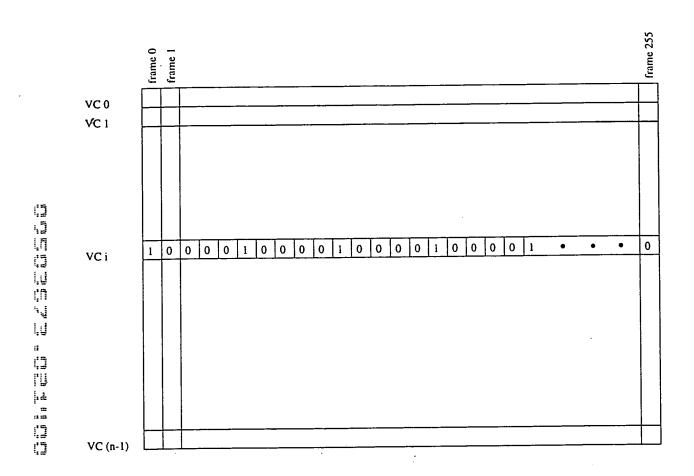
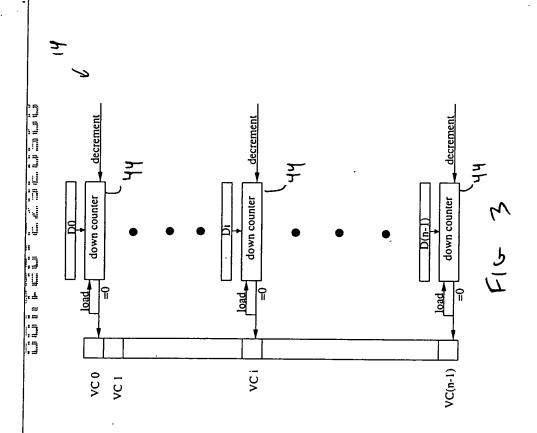
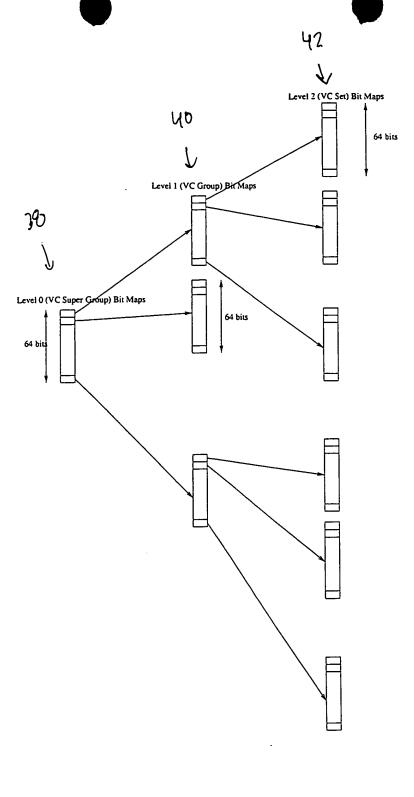
VC 0	w0
VC 1	wl
·	
VC (n-1)	w(n-1)

FIG 1

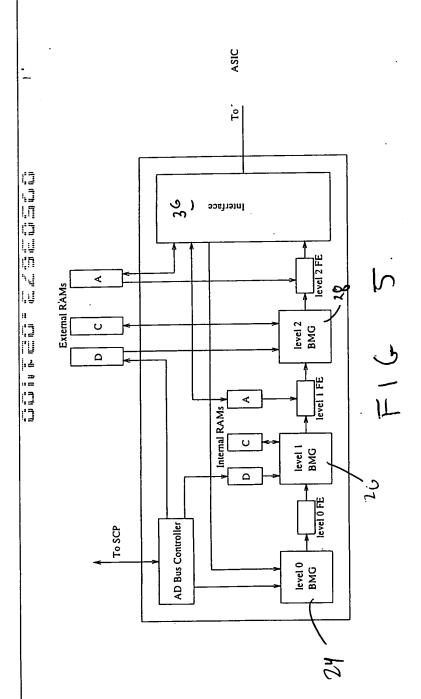


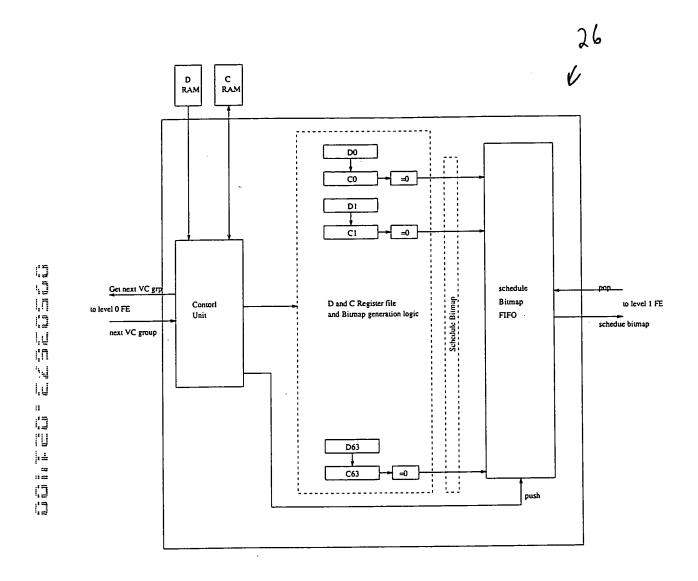
F16 2



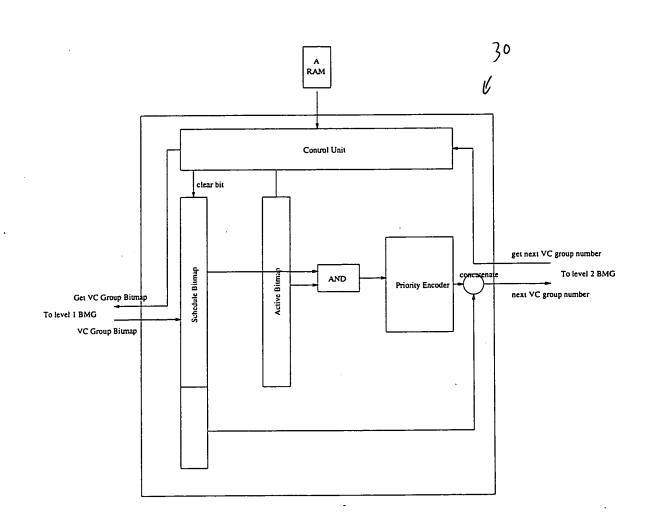


T15 4

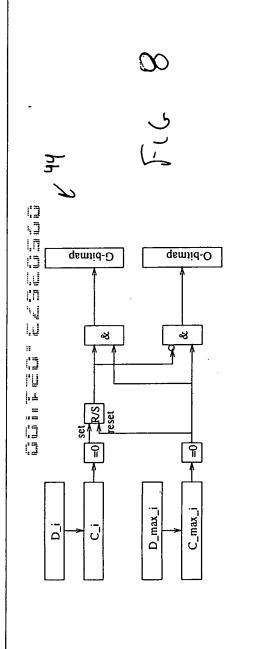


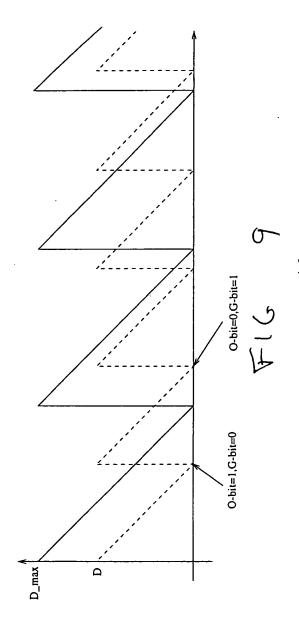


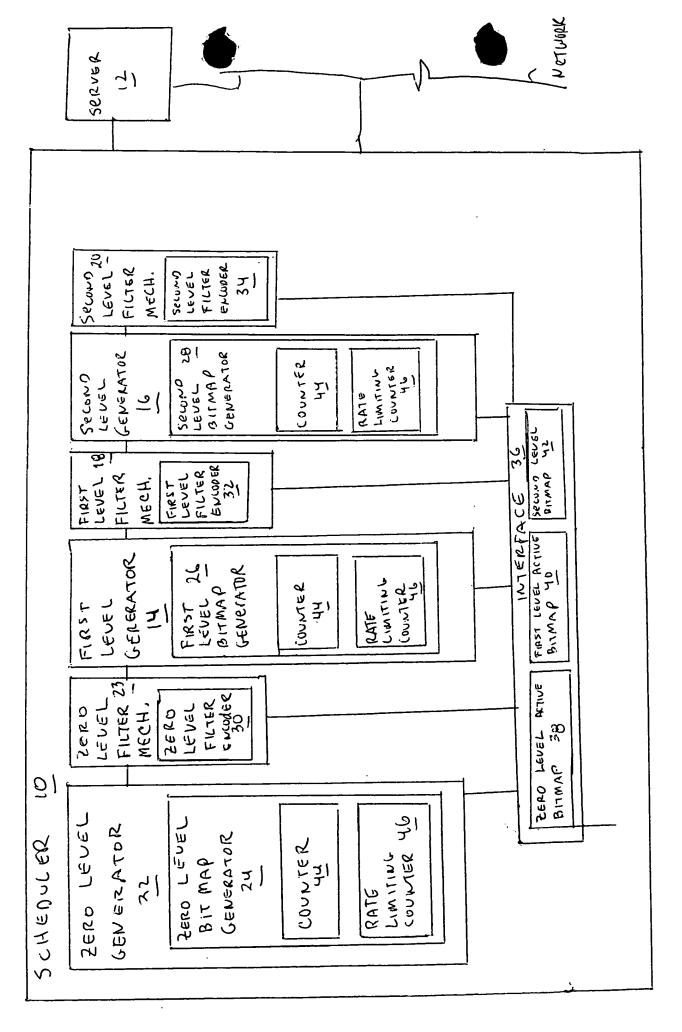
F16 6



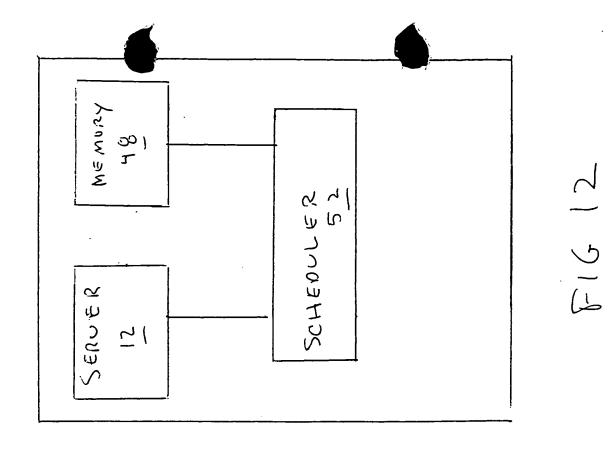
F16 7







01 511



Server Memory
12
HIERARCHICAL
SCHEDULER
50

11 51-